

524/920

09/843630	09/843630
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED <i>883</i> Q.A. <i>1c</i>	PATENT DATE
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APPLICATION NO. 09/843630	CONT/PRIOR F	CLASS 428 257	SUBCLASS 787	ART UNIT 2812 2827	EXAMINER <i>Mitchell</i>
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APPLICANTS

Kazuo Nishiyama

TITLE

Chip-like electronic components, a method of manufacturing the same, a pseudo wafer therefor and a method of manufacturing thereof

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL			CROSS REFERENCE(S)						
CLASS		SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION									
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig. <i>1K</i>	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE FEE	
				Amount Due	Date Paid
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